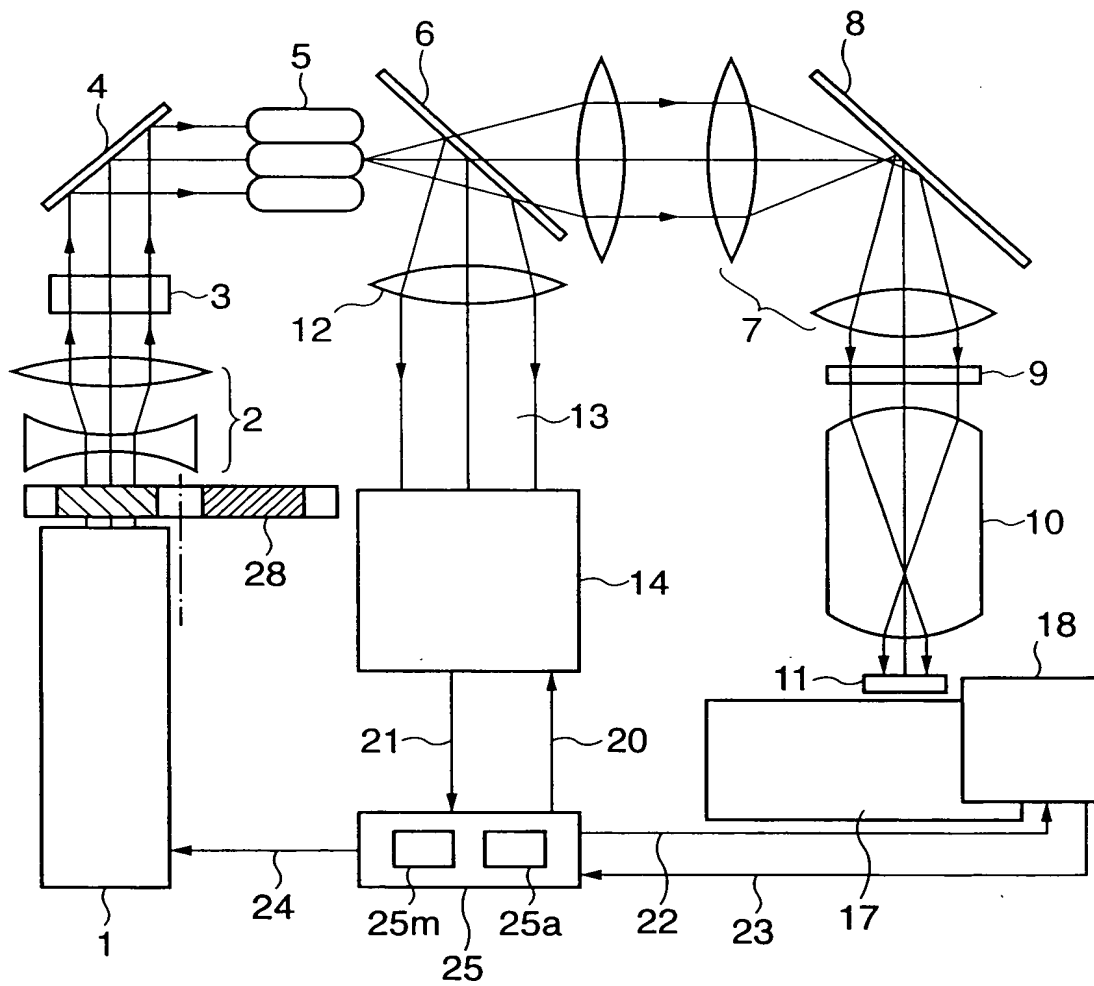




FIG. 1





2/7

**FIG. 2**

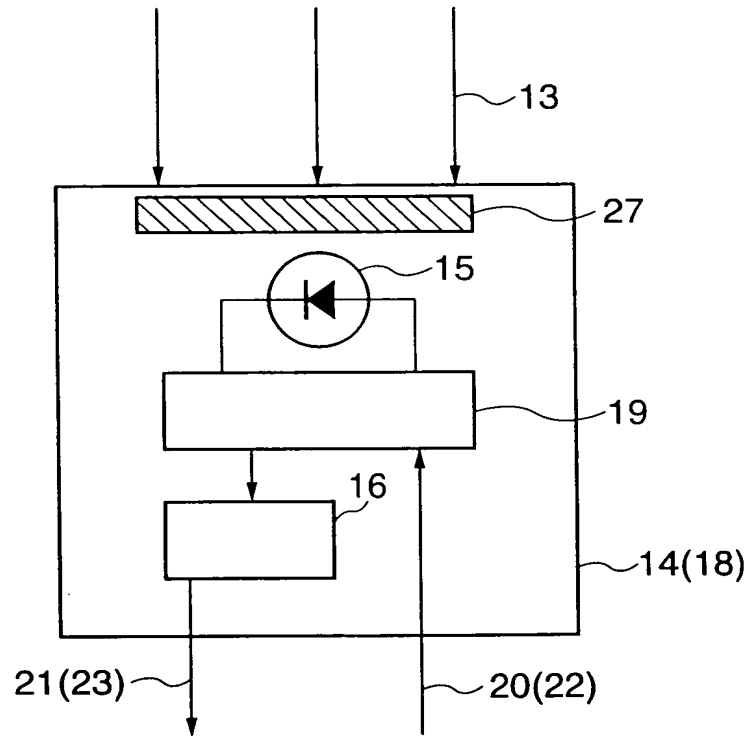




FIG. 3

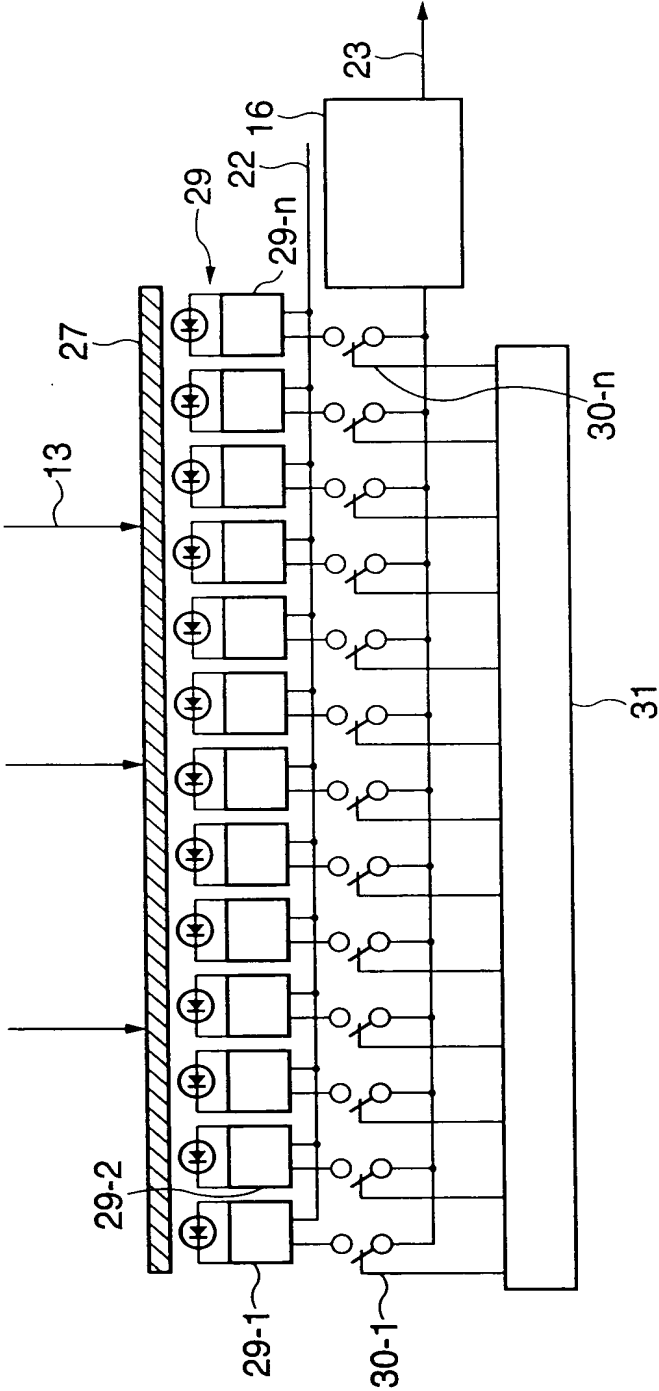
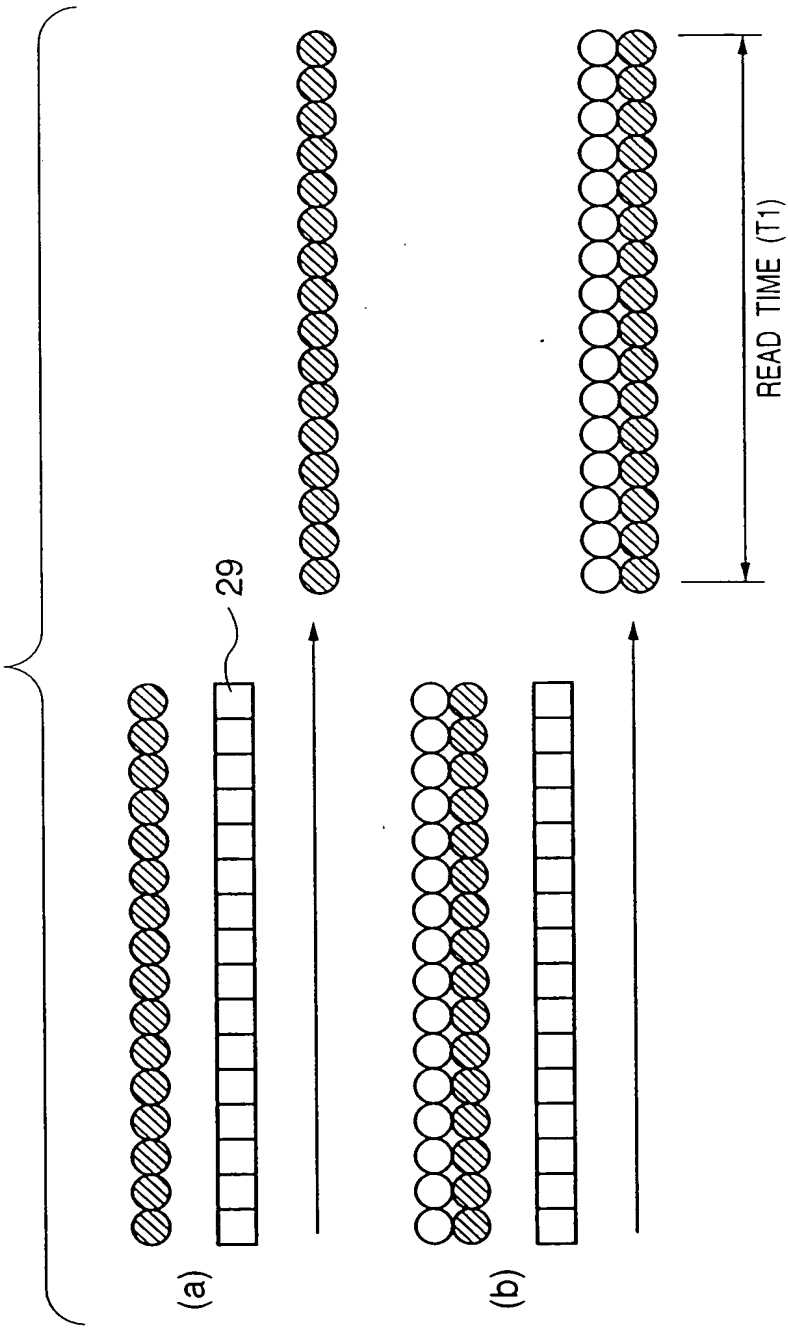




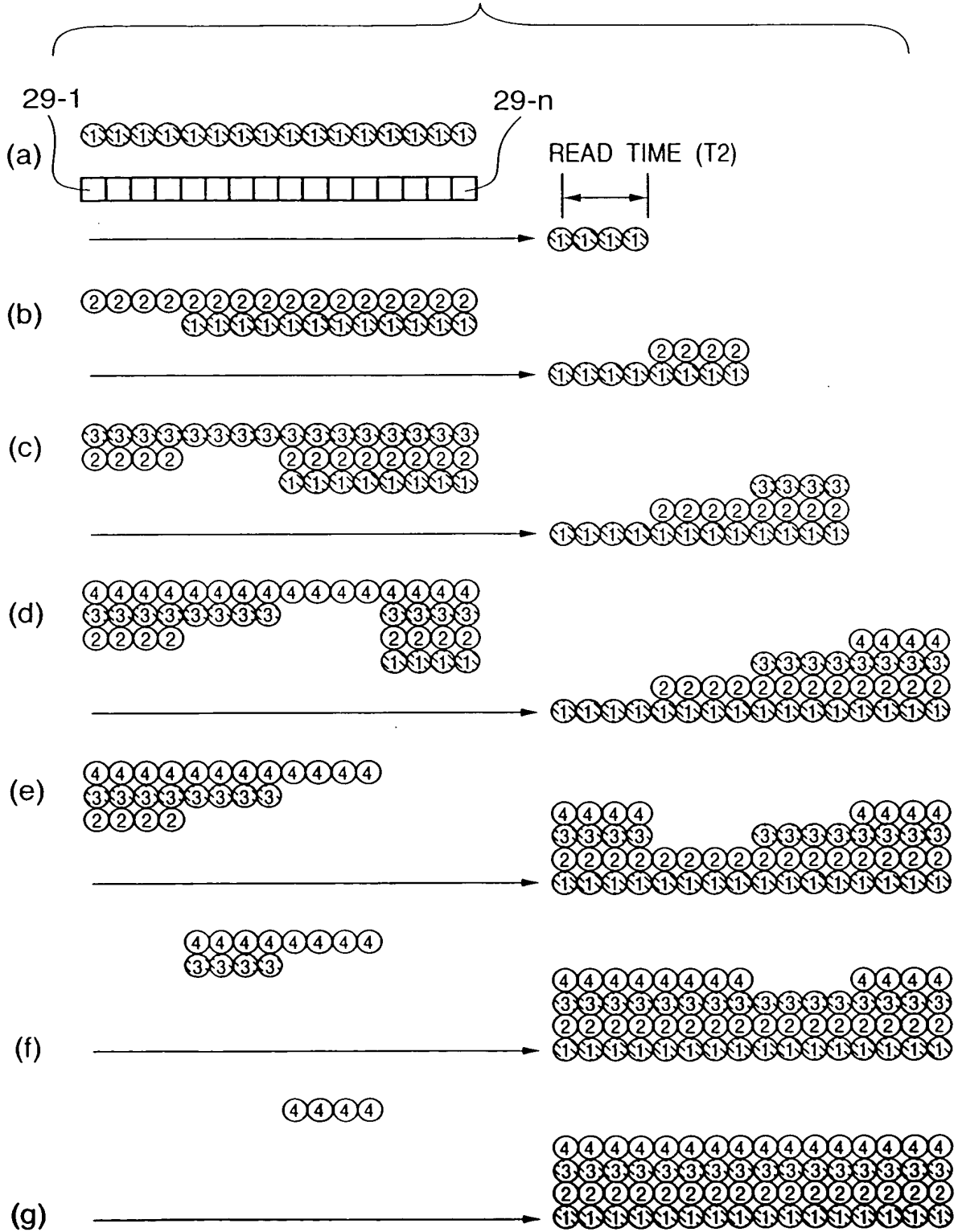
FIG. 4

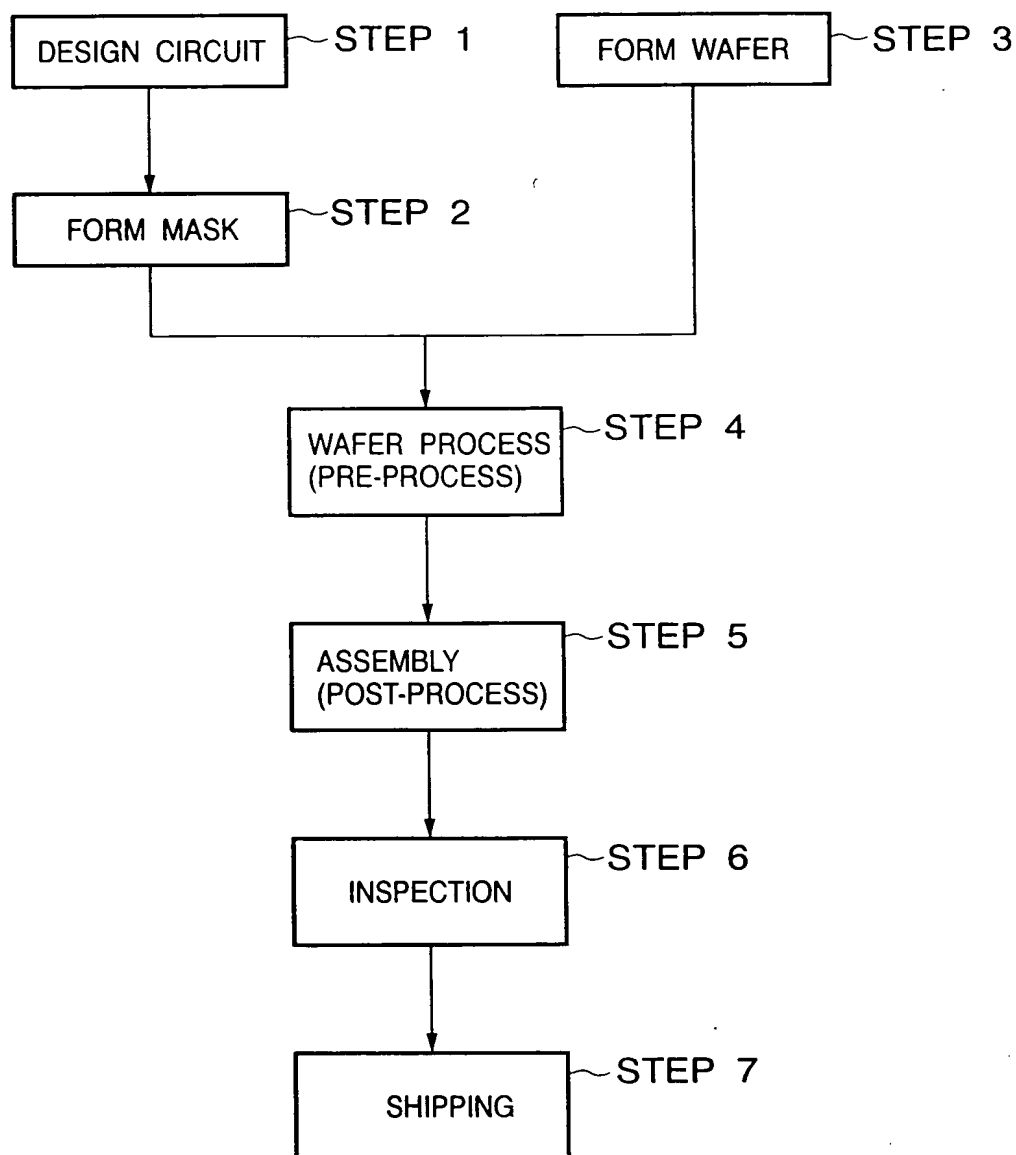


# REPLACEMENT SHEET

5/7

## FIG. 5



**FIG. 6**

SEMICONDUCTOR DEVICE MANUFACTURING FLOW



7/7

FIG. 7

